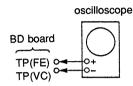
SECTION 4

ELECTRICAL BLOCK CHECKING

Note:

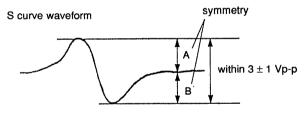
- 1. CD Block basically constructed to operate without adjustment. Therefore, check each item in order given.
- Use YEDS-18 disc (3-702-101-01) unless otherwise indicated.
- 3. Use the oscilloscope with more than $10M\Omega$ impedance.
- Clean an object lens by an applicator with neutral detergent when the signal lever is low than specified value with the following checks.

S Curve Check



Procedure:

- 1. Connect oscilloscope to test point TP (FE) on BD board.
- 2. Connect between test point TP (FEI) and TP (VC) by lead wire.
- 3. Turned Power switch on.
- 4. Put disc (YEDS-18) in and turned Power switch on again and actuate the focus search. (actuate the focus search when disc table is moving in and out.)
- Check the oscilloscope waveform (S curve) is symmetrical between A and B. And confirm peak to peak level within 3±1 Vp-p.

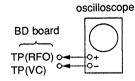


6. After check, remove the lead wire connected in step 2.

Note:

- Try to mesure several times to make sure than the ratio of A: B or B: A is more than 10:7.
- Take sweep time as long as possible and light up the brightness to obtain best waveform.

RF Level Check



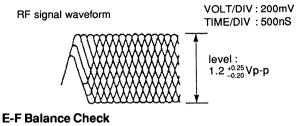
Procedure:

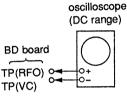
- 1. Connect oscilloscope to test point TP (RFO) on BD board.
- 2. Turned Power switch on.

- 3. Put disc (YEDS-18) in and playback.
- Confirm that oscilloscope waveform is clear and check RF signal level is correct or not.

Note:

Clear RF signal waveform means that the shape "\$\infty\$" can be clearly distinguished at the center of the waveform.





Procedure:

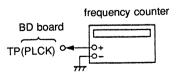
- Connect test point TP (ADJ) on MAIN board to ground and TP(TEI) to TP (VC) with lead wire.
- 2. Connect oscilloscpe to test point TP (TE) on BD board.
- 3. Turned Power switch on.
- 4. Put disc (YEDS-18) in and playback.
- Confirm that the oscilloscope waveform is symmetrical on the top and bottom in relation to OVdc, and check this level.

level: 2±1 Vp-p

6. Remove the lead wire connected in step 1.

RF Free-run Frequency Check Procedure:

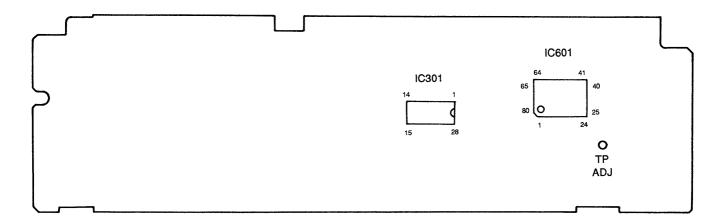
 Connect frequency counter to test point (PLCK) with lead wire.



- 2. Turned Power switch on.
- 3. Confirm that reading on frequency counter is 4.3218MHz.

Adjustment Location:

[MAIN BOARD] — Conductor Side —



[BD BOARD] — Conductor Side —

