| Design04 - PSoC Creator 2.2 [0   | C:\Travail_eleves\ESSAIS\Design04\Design04\cydsn\main.c]   |
|----------------------------------|--|
| <u>File Edit View Project B</u>  | uild <u>D</u> ebug <u>T</u> ools <u>W</u> indow <u>H</u> elp   |
| ST P & B B B B B B B B           | ※ 🖻 🗈 × 「 ヴ マ 🖕 📴 マ 🖕 Debug - 🖕  |
|                                  |  |
| ₩•≧ॐ៥ ₩≹ ,                       |  |
| Vorkspace Explorer               | ↓ ↓ Start Page TopDesign.cysch main.c ↓ ↓  |
| 🖪 🕿                              |  |
| Workspace 'Design04' (1 Projects |  |
| Project 'Design04' [CY8C!        | 568AX1-060 (a) 3 * Copyright YOUR COMPANY, THE YEAR  |
| TopDesign.cysch                  | 4 * All Rights Reserved  |
| Design04.cydwr                   | 5 * UNPUBLISHED, LICENSED SOFTWARE.  |
| 🖻 🍘 Header Files                 |  |
| h device.h                       | 9 7 * CONFIDENTIAL AND PROPRIETARY INFORMATION   |
| Source Files                     | 8 * WHICH IS THE PROPERTY OF your company.   |
| i comain.c                       | <pre>Output for the second sec</pre> |
|                                  |  |
|                                  |  |
|                                  | F 12 #include <device.h></device.h>  |
|                                  | <pre>3 int entier=0;</pre>   |
|                                  |  |
|                                  | n l5 void main()   |
|                                  |  |
|                                  | <pre>17 /* Place your initialization/startup code here (e.g. MyInst_Start()) */</pre>  |
|                                  |  |
|                                  | 19 /* CyGlobalIntEnable; */ /* Uncomment this line to enable global interrupts. */   |
|                                  | 20 for(;;)   |
|                                  |  |
|                                  | 22 /* Place your application code here. */   |
|                                  | 23 entier = 5;   |
|                                  |  |
|                                  | Output   |
|                                  | Show output from: All  |
|                                  | Log file for this session is located at: C:\Users\eleves.STI2DSIN 1 02\AppData\Local\Temp\PSoC Creator-000.log   |
|                                  | Build Started: 01/08/2057 12:54:55 Project: Design04, Configuration: ARM GCC 4.4.1 Debug   |
|                                  | cydsfit.exe "appdatapath" "C:\Users\eleves.STI2DSIN 1 02 AppData\Local\Cypress Semiconductor\PSoC Creator\2.2" "fdsnotice" "fdswarpdepfile=warp dependen   |
|                                  | Elaborating Design   |
|                                  | ADD: prj.M0246: information: PSoC 5LP provides many improvements over PSoC 5. Learn more about migrating to PSoC 5LP at www.cypress.com/go/PSoC5LP   |
|                                  | * Design04 ()  |
|                                  | HDL Generation   |
|                                  | Synthesis  |
|                                  | Design04.v (line 1, col 1): (E371) Can't find 'std logic 1164' in library 'ieee' with path 'ieee'.   |
|                                  | Design04.v (line 1, col 1): (E371) Can't find 'vlg syn' in library 'cypress' with path 'cypress'.  |
|                                  | Dependency Generation  |
|                                  | Clean Temporary Files  |
|                                  | Error: cdf.M0005: CyDsFit aborted due to errors, please address all errors and rerun CyDsFit. (App=cydsfit)  |
|                                  |  |
|                                  |  |
|                                  |  |

For this computer the %USERNAME% is **eleves** and not **eleves.STI2DSIN\_1\_02** 

## Eleves is the session name

But the computer is logging with **eleves.STI2DSIN\_1\_02** where **STI2DSIN\_1\_02** is the netname so there is a problem

I don't know where is the mistake with this PSoC Creator configuration.

Regards

P.Guerange